

UNITED STATES PATENT APPLICATION
IMPROVED HYBRID BATTERY POWER SOURCE
FOR IMPLANTABLE MEDICAL USE

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CROSS-REFERENCE TO RELATED APPLICATIONS

This application is a continuation-in-part of United States Patent Application Serial No. 10/727,349, filed on December 3, 2003 and entitled "Improved Hybrid Battery Power Source For Implantable Medical Use," which is a continuation-in-part of United States Patent Application Serial No. 10/407,916, filed on April 4, 2003 and entitled "Hybrid Battery Power Source For Implantable Medical Use," which is a continuation-in-part of United States Patent Application Serial No. 10/350,921, filed on January 24, 2003 and entitled "Hybrid Battery Power Source For Implantable Medical Use."

10 BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates to improvements in the performance of implantable defibrillators, implantable cardioverter-defibrillators (ICDs) and other battery powered medical devices designed to provide high energy electrical stimulation of body tissue for therapeutic purposes.

2. Description of Prior Art

High energy battery powered medical devices, such as implantable defibrillators and ICDs, are designed to produce a strong electrical shock to the heart when called upon to correct the onset of tachyarrhythmia. The shock is produced by one or more energy storage capacitors that have been charged to a high voltage by the device's battery power source. The power source is typically a lithium/silver vanadium oxide (Li/SVO) battery or cell of the type disclosed in U.S Patent No. 5,458,997 of Crespi, and references cited therein. Crespi notes that the Li/SVO chemistry is useful for defibrillation applications because of its ability to produce pulses of energy that can charge the high voltage capacitors within the short time frame required by the device. In particular, the Li/SVO battery is typically called upon to charge the capacitors to deliver within 10 seconds or less a shock of up to 40 Joules. This must be done several times in succession if additional shocks are required. Unfortunately, as noted by Crespi, a Li/SVO cell can experience unpredictable resistance increase upon long-term discharge service. In particular, Li/SVO cells commonly have a two-stage run down with slightly different voltage plateaus at each stage. It is at the interval between the two plateaus where it is common to see the resistance increase described by Crespi. The problem is further explained in U.S. Patent No. 6,426,628 of Palm et al. as being a transient

phenomenon that occurs following a period of low current draw. When a load is reapplied (e.g., a defibrillation pulse is required), the resistance build-up temporarily prevents the cell from developing its full open circuit voltage potential. This condition, which is referred to as "voltage delay," continues for a brief period until the resistance diminishes back to some

5 nominal level.

In many cases, the voltage delay experienced by a Li/SVO cell is significant enough to impair the cell's ability to charge the capacitors of a defibrillator or ICD in a timely manner. This may result, prematurely, in a decision being made that the Li/SVO cell has reached end of service (EOS) and needs to be explanted for replacement. In addition to the

10 patient inconvenience and risk entailed by the explantation procedure, a significant portion of the capacity of the Li/SVO cell is needlessly rendered unavailable for long-term use. Even if it is not removed, the cell's operation is unpredictable, thus making any attempt to calculate the EOS point rather complicated.

Additional shortcomings in the application of Li/SVO cells have been previously identified in U.S. Patent No. 5,674,248 of Kroll et al. A typical Li/SVO cell suitable for the described applications has a volumetric energy density of about 0.7 watt-hours per cubic centimeter (Wh/cm³) as compared to 1.0 Wh/cm³ for lithium/carbon monofluoride (Li/CFx) cells and 0.9 Wh/kg for lithium/iodide (Li/I) cells, the latter being used almost exclusively for implantable pacemakers. This energy density disadvantage requires the use of a battery with greater volume and weight than would otherwise be needed if the Li/CFx or Li/I cells could be used. However, the Li/CFx and Li/I cells are unsuitable for the described applications because they cannot support the rapid discharge rates required for charging the defibrillator capacitors. The Li/SVO cell has the added disadvantage of significantly higher cost when compared to the Li/CFx and Li/I chemistries.

The Kroll et al. patent proposes a staged energy concentration system for providing improved energy sources and device performance. A first energy stage utilizes a Li/CFx or Li/I battery to implement a primary power source. The first energy stage provides power to a second energy stage that utilizes either a lithium-based rechargeable secondary battery or a high energy density capacitor system. Energy is transferred at a low-rate from the first energy stage comprising the primary battery to the second energy stage comprising the rechargeable battery or the high energy density capacitor system. A trickle charge control circuit and a voltage doubler circuit are alternatively shown being interposed between the

first and second energy stages. The second energy stage is rapidly discharged upon the detection of fibrillation to develop the high voltage charge needed for defibrillation therapy.

A shortcoming of the Kroll et al. high energy density capacitor system is the volume and number of capacitors needed for the second stage to support the storage of energy

5 required, typically 200 -300 Joules for a series of five therapeutic countershocks which might be required in a span of less than one minute. A shortcoming of the rechargeable battery system is that the lithium-based battery chemistries proposed for the secondary energy stage are not all suitable for the proposed application. Table 1 below sets forth the proposed chemistries. One is an LiMnO_2 system, but this is a primary system and is not suited to 10 recharging. Another is an LiSO_2 system, but this operates with a sealed cell at a pressure of 3 to 6 atmospheres and is not suited for high-rate discharge applications. The remaining identified chemistries, namely LiMoS_2 , LiV_2O_5 , LiTiS_2 , $\text{LiV}_6\text{O}_{13}$, LiCuC_{12} , as well as NiCad, Alkaline and Lead acid, have not found wide acceptance in the implantable device market.

Table 1

15 Second Stage Candidate Cells Identified in U.S. Patent No. 5,674,248

Chemistry	Cell Voltage (VDC)
LiMoS_2	1.85
LiMnO_2	3.0
LiV_2O_5	2.8
LiTiS_2	2.2
$\text{LiV}_6\text{O}_{13}$	2.3
LiCuC_{12}	3.2
LiSO_2	3.1

A broader shortcoming is the failure of the Kroll et al. patent to identify a specific selection for a first stage battery and a configuration for the identified trickle charge control circuit or the voltage doubler circuit. A Li/I battery with a beginning of life open circuit 20 voltage of 2.8 volts DC is identified within the disclosure as a potential candidate for the first energy stage. The output voltage of this cell falls to about 2.6 volts at EOS so the cell would be incapable of charging LiMnO_2 , LiV_2O_5 , LiCuC_{12} and LiSO_2 cells unless the trickle charge control circuit utilized a means of increasing the first stage output voltage. This is clear

because each of these cells has a higher operating voltage than the Li/I battery. The voltage doubler circuit is used in conjunction a first stage primary battery continuously recharging a second stage rechargeable battery. The Kroll et al. disclosure does not identify a method or means of controlling the flow of energy while recharging that is necessary to prevent damage to or catastrophic failure of the second stage cell or cells.

5 A need therefore exists for improvement in defibrillator/ICD battery power systems so to overcome the above-described deficiencies of the prior art.

SUMMARY OF THE INVENTION

The foregoing problems are solved and an advance in the art is provided by a novel 10 hybrid battery power source for high energy battery powered medical devices, such as implantable defibrillators and ICDs. The hybrid battery power source has relatively constant charge time characteristics and is not affected by the voltage delay phenomenon associated with Li/SVO batteries. In addition, the hybrid battery power source provides a significant improvement in the stored energy density for an implantable power source that is suitable for 15 high energy battery powered medical devices. Finally, the hybrid battery power source overcomes the limitations of staged energy conversion systems that have been previously disclosed.

In exemplary embodiments of the invention, the power source utilizes two batteries, each of which may comprise one or more cells. The first battery is a primary 20 (nonrechargeable) battery of relatively high energy density. The second battery is a secondary (rechargeable) battery whose internal resistance is relatively low and stable over time. The primary battery and the secondary battery are connected in a parallel arrangement via a charge control circuit. The charge control circuit may include a voltage boost function and is adapted to limit the charge/discharge excursions and control the recharging rate of the 25 secondary battery in a manner that optimizes its output for high energy medical device use.

It is therefore an object of the present invention to minimize the effect of the change in internal resistance as a battery for implantable medical use discharges during service.

A further object of the invention is to add a secondary power segment having low 30 internal resistance and relatively constant terminal voltage to an implantable power source that will provide a source for rapid charging of energy storage capacitors used to deliver high energy impulses.

A still further object of the invention is to optimize the performance of a secondary power segment for use in a high energy medical device environment.

BRIEF DESCRIPTION OF THE DRAWINGS

The foregoing and other features and advantages of the invention will be apparent 5 from the following more particular description of preferred embodiments of the invention, as illustrated in the accompanying Drawings in which:

Fig. 1 is a diagrammatic perspective view showing primary and secondary batteries in accordance with the invention;

Fig. 2 is a schematic diagram showing an exemplary hybrid battery power source 10 comprising a primary segment battery "A" and a secondary segment battery "B" connected by a voltage boost/charge control circuit having an inductor and a pulse generating control circuit therein.

Fig. 3A is a graph which shows the relationship between energy capacity and the number of charge/discharge cycles for a typical lithium-ion cell.

Fig. 3B is a graph which shows the relationship between cell internal resistance and the number of charge/discharge cycles for a typical lithium-ion cell.

Fig. 4 is a schematic diagram showing another exemplary hybrid battery power source comprising a primary segment battery "A" and a secondary segment battery "B" connected by another voltage boost/charge control circuit having a flyback transformer and a pulse 20 generating control circuit therein.

Fig. 5 is a schematic diagram showing another exemplary hybrid battery power source comprising a primary segment battery "A" and a secondary segment battery "B" connected by another voltage boost/charge control circuit having a non-inductive pulse-driven voltage booster and a pulse generating control circuit therein.

Fig. 6 is a schematic diagram showing additional details the voltage booster shown in Fig. 5, along with a charge controlling flip-flop.

Fig. 7 is a representative graph of cell voltage and charging current versus time for charging lithium-ion cells.

Fig. 8 is a schematic diagram showing another exemplary hybrid battery power source 30 comprising a primary segment battery "A" and a secondary segment battery "B" connected by another voltage boost/charge control circuit that incorporates active state-of-charge

monitoring and variable recharge rate control implemented with low-power analog and digital circuitry.

Fig. 9 is a schematic diagram showing another exemplary embodiment of a hybrid battery power source comprising a primary segment battery "A" and a secondary segment battery "B" connected by another voltage boost/charge control circuit that incorporates active state-of-charge monitoring and variable recharge rate control implemented with a low-power microprocessor.

Fig. 10 is a flowchart that describes exemplary logic for controlling the recharging rate of the secondary segment battery "B" of Fig. 9 based upon its state of charge.

Fig. 11 is a schematic diagram showing another exemplary embodiment of a hybrid battery power source comprising a primary segment battery "A" and a secondary segment battery "B" connected by another voltage boost/charge control circuit that incorporates a low-power microprocessor that monitors the state-of-charge of secondary segment battery "B" and effects active control of the rate of recharge, lower recharge threshold voltage and upper recharge threshold voltage.

Fig. 12 is a schematic diagram showing a hybrid battery power source of the invention arranged in a defibrillator and discharging into a voltage amplification system.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

Introduction

Exemplary hybrid battery power sources for use with implantable defibrillators, ICDs and other high energy battery powered medical devices will now be described, together with an exemplary defibrillator that incorporates a hybrid battery power source therein. As indicated by way of summary above, the power source embodiments disclosed herein are characterized by having primary and secondary batteries. The primary battery has high energy density but typically will also have internal resistance that is relatively high, or which can increase significantly during discharge. Examples of this primary battery include the lithium-carbon monofluoride (Li/CF_x) battery, the lithium-bromine chloride (Li/BrCl₂) battery, the lithium sulfuryl chloride (Li/SO₂Cl₂) battery, the lithium thionyl chloride (Li/SOCl₂) battery, the lithium-manganese dioxide battery (Li/MnO₂), and the lithium-silver vanadium oxide (Li/SVO) battery. The secondary battery is rechargeable and has low internal resistance that is relatively stable over time. It will typically also have relatively low energy density. Examples include the lithium-ion battery which typically utilizes lithium

cobalt oxide and carbon or graphite. The primary and secondary batteries are electrically connected in an indirect parallel arrangement by means of a voltage boost/charge control circuit designed to optimize the performance of the secondary battery for high energy implantable medical use by limiting its maximum charge state and discharge excursions.

5 In this indirect parallel connection arrangement of the primary and secondary batteries, a voltage boost/charge control circuit is interposed to increase the voltage supplied by the primary battery and to control the flow of energy supplied by the primary battery to the secondary battery. This voltage boost/charge control circuit operates independently of primary battery run-down. For example, in a hybrid battery power source comprising an
10 Li/CFx primary battery and a lithium-ion secondary battery interconnected by a voltage boost/charge control circuit, the principal energy supplied by the Li/CFx battery at about 2.8 volts can be increased to charge the lithium-ion battery to its normal open circuit voltage of about 4.0 volts, if desired. This charging voltage can be sustained throughout the life of the device even as the Li/CFx battery voltage falls during discharge to its typical end of life value
15 of 2.0 volts. In this way, the primary battery is used as a long term energy supply to recharge the secondary battery for service when and if its voltage drops below the desired level. The voltage boost/charge control circuit also serves to limit the current drawn from the primary battery during defibrillatory charging, ensuring that virtually all of the energy is drawn from the secondary battery. This protects the primary battery from excessive high rate discharge
20 that could lead to premature failure.

25 A third benefit provided by the voltage boost/charge control circuit is the regulation of the charging current and voltage that is supplied to the rechargeable secondary battery from the primary battery. In order to maximize secondary battery performance and minimize the risk of catastrophic cell failure, the voltage and current must be closely regulated throughout the life of both batteries, even as the primary battery voltage decays through normal discharge. In particular, the long-term performance of the secondary battery can be strongly influenced by the maximum state of charge (SOC) maintained on the battery and the discharge excursions that the battery experiences during use. For example, recharging and storing a lithium-ion secondary battery at or near 100% SOC and allowing significant
30 discharge before the initiation of recharge can significantly increase the rate and extent to which the battery energy capacity irreversibly fades during battery service life. Irreversible capacity fade in a lithium-ion battery reduces its available energy output, which could

decrease the number of high energy pulses available from an implantable medical device. Capacity fade has also been correlated to long term internal resistance increases. Though less than the internal resistance variance found in a typical Li/SVO primary cell designed for implantable medical use, the internal resistance increase of a lithium-ion secondary cell could 5 nonetheless impact the high energy pulse generating cycle of an implantable medical device. As such, the voltage boost/charge control circuit is adapted to limit the maximum SOC and allowable discharge excursions of the secondary battery to optimal levels.

A fourth benefit provided by the voltage boost/charge control circuit is the active control of the recharging rate of the secondary battery based upon its SOC and the time rate-10 of-change of its SOC. By periodically sampling the secondary cell voltage, the SOC and rate of change of the SOC may be calculated and the recharging rate of the cell may be adjusted in order to better meet the intermittent periods of high discharge rate which are characteristic of multiple successive cardiac stimulation episodes. This is especially useful for extreme use conditions where eight or more defibrillatory shocks may be required within a two-hour 15 period.

The hybrid power source embodiments disclosed herein can be designed for maximum service life by selecting the primary and secondary batteries so that the former provides most of the combined battery capacity while still allowing the latter to power a reasonable number of defibrillatory charging cycles prior to complete recharging, say 50 20 cycles. By way of example, the secondary battery could be selected to provide 10% of the total capacity of the power source, with the primary battery providing the remaining 90%.

If an implantable defibrillator or ICD is provided with one of the above-mentioned primary batteries and a lithium-ion secondary battery arranged in the manner disclosed herein, the lithium-ion battery will be the predominant energy source for charging the 25 defibrillator's energy storage capacitors during the defibrillation cycle. Since this battery would always be charged to an energy capacity sufficient to support a predetermined number of defibrillatory cycles, the charge state and voltage of the primary battery would not be a factor in the ability of the device to quickly deliver defibrillation impulses. Thus, advantage can be taken of the superior energy density properties of the primary battery while also 30 providing the high rate discharge capabilities required for defibrillators and ICDs. The manufacturer can rely on a lithium-ion battery having no voltage delay properties of its own,

and comparatively stable internal resistance and charge state, to supply the defibrillation energy rapidly and consistently.

An additional benefit of this use of a lithium-ion secondary battery in conjunction with a voltage boost/charge control circuit is the higher source voltage available to the defibrillator inverter circuitry that charges the energy storage capacitors. The higher source voltage mitigates the effects of circuit current/resistance losses in the inverter that become more significant as the source voltage decreases. The lithium-ion battery voltage is typically 3.5 to 4.0 volts as opposed to 2.0 to 3.0 volts for a Li/SVO cell of the type presently used in implantable defibrillators and ICDs. Also, the lithium-ion cell provides a significant advantage over other lithium chemistries such as LiV_2O_5 at 2.8 volts and LiTiS_2 at 2.2 volts, which are identified as candidate secondary batteries in U.S. Patent No. 5,674,248 of Kroll et al., discussed by way of background above.

Illustrated Embodiments

Referring now to the Drawings in detail wherein like reference numerals have been used throughout the various figures to designate like elements, there is shown in Fig. 1 an illustration of two segments of a hybrid battery power source 2, namely, a primary battery "A" and a secondary battery "B". By way of example only, the primary battery "A" will be assumed to incorporate either a Li/CF_x , Li/BrCl_2 , Li/MnO_2 , $\text{Li/SO}_2\text{Cl}_2$, Li/SOCl_2 or Li/SVO battery chemistry, and the secondary battery "B" will be assumed to incorporate a rechargeable lithium-ion chemistry. Such batteries are standard commercial products that are available from Wilson Greatbatch Technologies, Inc. of Clarence, New York as catalog items. Figure 1 also depicts that for each of battery "A" and battery "B", one electrical connection can be made via the vertical pins 4 and 6 respectively protruding from the top of cases 8 and 10. The pins 4 and 6 represent the positive (cathode) terminals of the batteries. A second electrical connection can be made to the cases 8 and 10 themselves, and case contacts 12 and 14 are respectively shown. These case contacts represent the negative (anode) terminals of the batteries.

Turning now to Fig. 2, an exemplary hybrid battery power source 20 is shown. This power source includes the primary battery "A" and the secondary battery "B" of Fig. 1. Battery "A" and battery "B" are connected in parallel with a switching-type voltage boost/charge control circuit 22 interposed between the two batteries to charge the secondary battery "B" to a higher potential than the primary battery "A" and to regulate the applied

charging current and maximum cell charge/discharge voltage excursions. By way of example, a 2.8-volt Li/CFx or 3.5 volt Li/BrCl₂ or 3.0 volt Li/MnO₂ or 3.5 volt Li/SO₂Cl₂ or 3.5 volt LiS/OCl₂ or 2.6-volt Li/SVO primary battery "A" will have its voltage boosted to about 3.8 to 4.0 volts for recharging a lithium-ion secondary battery "B." Within the circuit

5 22 is a conventional oscillator-type pulse control circuit 24 that provides variable width/duty cycle charging pulses to charge the secondary battery "B" to a relatively constant potential even though the voltage level of the primary battery "A" may vary through some range. By way of example, if the primary battery "A" is an Li/SVO or Li/CFx battery and the secondary battery "B" is a lithium-ion battery, the charging voltage on the secondary battery "B" can be
10 maintained at a fairly constant level of 3.5 to 4.0 volts even though the output of the primary battery "A" varies between about 2.0 to 3.0 volts during its service life. Thus, within the life of the primary battery "A," every defibrillator charging cycle will occur at nearly the same rate due to the regulated charge state and, hence, constant voltage output of the secondary battery "B."

15 In Fig. 2, the primary battery "A" delivers energy to the input of the circuit 22 while the output of the circuit 22 is connected to the secondary battery "B." The basic principle of operation for the circuit 22 is as follows:

- The control circuit 24 derives prime power from the primary battery "A" and includes an oscillator function. The control circuit's output labeled "Switch" is toggled rapidly (e.g. 20 100 kHz) to turn the field effect transistor Q1 on and off.
- Each time that transistor Q1 is turned on, current flow increases in the inductor L1 and energy is imparted to the magnetic field associated with the inductor. A rectifier diode D1 is reverse biased and prevents current flow from the secondary battery "B" backward in to Q1.
- When transistor Q1 is turned off, the magnetic field collapses in the inductor L1 and returns the energy to the circuit, causing the voltage across the inductor to increase.
- The collapsing magnetic field in the inductor develops a higher positive voltage at the anode of rectifier diode D1. The diode D1 becomes forward biased and current flows from the inductor L1 through the diode and into the secondary battery "B," transferring 30 energy to that battery.

- The circuit labeled "Feedback" samples the voltage at the secondary battery "B" and controls the rate of switching of transistor Q1 to regulate the flow of energy to battery "B" and, hence, the charge imparted to that battery.
- This feedback voltage is also applied to a conventional window voltage comparator 25 which compares the battery "B" voltage against two predetermined reference voltages "Vth_low" and "Vth_high" 26 which, by way of example, might be 3.7 and 3.9 volts respectively. When the terminal voltage of battery "B" falls below "Vth_low" the first comparator output is asserted. This output is connected to the "Set" input of Flip-Flop 23 so that the Flip-Flop is toggled to the set state.
- Similarly, when the terminal voltage of battery "B" rises above "Vth_high" the second comparator output is asserted. This output is connected to the "Reset" input of flip-flop 23 so that the flip-flop is toggled to the reset state. This configuration results in flip-flop 23 being toggled to the "set" state when the battery "B" voltage falls below a first predetermined value and being toggled to the "reset" state when the battery "B" voltage rises above a second predetermined value.
- The output "Q" of flip-flop 23 is connected to the "Enable" input of the control circuit 24. This function provides the control required via the circuit labeled "Enable" to enable the control circuit 24 only if the battery "B" voltage falls below a minimum value and to continue to charge battery "B" only until its terminal voltage has risen to a maximum acceptable value. At that point the "Enable" signal will be negated to prevent overcharging of battery "B".
- The flip-flop 23 is implemented with metal oxide semiconductor field effect transistors (MOSFETs) or similar devices which require little or no bias current to effect device switching in order to reduce quiescent current drain to an absolute minimum.
- An additional benefit of this circuit not previously disclosed is provided by the pulse generator 27. The charge control circuitry must derive prime power from one of the two batteries, in this case battery "A". In order to minimize the current drain on battery "A" needed to support the circuitry, the voltage reference 26 and window comparator 25 are powered by the output of the pulse generator 27. The pulse generator 27 is similar to oscillator circuits well known in the pacemaker industry, whereby energy is slowly extracted from a battery and used to charge a capacitor. The capacitor is then rapidly discharged to deliver a higher amount of power in a shorter period of time. In this

embodiment the pulse generator 27 output is used to power the voltage reference 26 and window comparator 25 circuitry so that the circuitry is only energized periodically, e.g. once per minute.

By intermittently energizing the voltage reference 26 and window comparator 25 and controlling the state of the control circuit 24 with a low power flip-flop 23, this embodiment significantly reduces the quiescent current drain on the battery and therefore provides a significant improvement in service life.

As indicated, the primary benefit of the hybrid battery power source 20 of Fig. 2 is that the secondary battery "B" can be charged and maintained at a higher working voltage than the primary battery "A," and virtually all of the capacitor charging energy can be derived from the secondary battery. By using voltage boosting and control circuitry having an inductive component connected between the primary and secondary batteries, increased voltage is delivered to the secondary battery "B" for recharging it to above the voltage of the primary battery "A." Moreover, the charging current is controlled by means of a variable pulse width and/or duty cycle control, allowing it to operate over a range of voltages on the primary battery "A," such as about 2.0 volts to 3.0 volts for an Li/SVO or Li/CFx battery, in all cases charging the secondary battery "B" to a higher voltage, such as 3.5 to 4.0 volts for a lithium-ion battery. Thus, throughout the life cycle of the primary battery "A," the secondary battery "B" will be delivering the same voltage to charge the capacitors in the defibrillator or ICD. This represents a significant advantage over prior art power sources, both in the short charge time and in the constancy of the charge going to the secondary battery "B." The full charge capacity of the primary battery "A" is thus available. The pulse width and/or duty cycle of the pulse control circuit 24 is simply varied to maintain a constant charging voltage on the secondary battery "B" regardless of where the primary battery "A" is in its life cycle.

Note that for a lithium-ion secondary battery "B" charged to 4.0 volts and operating at a current of 3.0 amperes with a conversion efficiency of 67 percent, a five second charge time is all that would be required to develop a 40 joule pulse. Again, this minimal charge time is a distinct improvement over previous defibrillator batteries.

Another benefit of the hybrid battery power source 20 that has not been previously disclosed in the prior art is the capability to regulate the flow of recharge energy from the primary battery "A" to the secondary battery "B." Regulation of the rechargeable battery charge state is a necessity for optimum energy storage because of the effects of irreversible

discharge capacity fade and increased cell internal resistance as a lithium-ion rechargeable cell is cycled. In particular, as reported by Spotnitz, R., "Simulation of Capacity Fade in Lithium-ion Batteries", *Journal of Power Sources*, 113, 72-80, (2003), the maximum SOC applied to a lithium-ion cell as well as the depth of discharge affects capacity fade and

5 internal resistance. In conventional rechargeable battery applications where the batteries may be readily replaced, the practice is to maximize the charge and depth of discharge in order to obtain maximum output capacity from the battery. However, in high energy medical device applications, applicants have determined that limiting maximum SOC and depth of discharge are necessary in order to ensure optimal pulse delivery characteristics. For charging to a

10 maximum SOC of 3.9 V and attempting to limit the depth of discharge to Δ 0.2 V (3.7 V minimum SOC) should provide adequate energy output while minimizing capacity fade and internal resistance increase over time. Additional data on the relationship between SOC and internal resistance for lithium-ion batteries has been reported by Wright, R.B. et.al. in

15 "Calendar- and cycle-life studies of advanced technology development program generation I lithium-ion batteries", *Journal of Power Sources*, 110, 445-470, (2002).

Referring now to Fig. 3A, a graph is shown of the discharge capacity retention versus the number of charge/discharge cycles for a representative lithium-ion battery. When first placed into service the battery provides a maximum energy storage capacity that diminishes as the battery is subjected to charging and discharging cycles. Reduced discharge capacity in

20 the secondary battery will manifest itself as a reduction in the number of defibrillator charges that may be delivered before the secondary battery requires charging. This degradation in discharge capacity is a strong function of the maximum SOC and depth of discharge that the battery is subjected to in each cycle. This degradation can be minimized by configuring the voltage boost/charging control circuit 20 to limit the charge/discharge excursions during the

25 device life.

Referring now to Fig. 3B, a graph is shown of the cell internal resistance versus the number of charge discharge cycles for a representative lithium-ion battery. When first placed into service the battery exhibits minimum internal resistance that increases as the battery is subjected to charging and discharging cycles. Increased cell internal resistance in the

30 secondary battery will manifest itself as an increase in the time required to fully charge the defibrillator energy storage capacitors because of the diminished ability to deliver high discharge currents. This increase in cell internal resistance is again a strong function of the

maximum SOC and depth of discharge that the battery is subjected to in each cycle. This degradation can therefore be minimized by configuring the voltage boost/charging control circuit 20 to limit the charge/discharge excursions during the device life.

Turning now to Fig. 4, another exemplary hybrid battery power source 30 is shown.

5 This power source includes the primary battery "A" and the secondary battery "B" of Fig. 1. Battery "A" and battery "B" are connected in parallel with a voltage boost/charge control circuit 32 containing a conventional oscillator-type pulse control circuit 34, but with the voltage boost function being implemented with a flyback converter. In particular, instead of utilizing a series inductor to boost the charging voltage to battery "B," a transformer T1 with
10 primary and secondary connections is used. Although this represents a minor complication in circuitry, it permits complete isolation of the circuits connected to the primary and secondary sides of the transformer. In Fig. 4, the primary battery "A" again provides the prime power for the circuit, and its output is connected to the input of the circuit 32. The pulse output of the circuit 32 charges the secondary battery "B." The basic principles of operation for the
15 circuit 32 are as follows:

- The control circuit 34 derives prime power from the primary battery "A" and again includes an oscillator function. The control circuit's output labeled "Switch" is toggled rapidly (e.g. 100 kHz) to turn the field effect transistor Q1 on and off.

20 • Each time that transistor Q1 is turned on, the current in the primary winding of transformer T1 increases and energy is stored in the magnetic field associated with the transformer. The connection of the secondary winding is reversed in the sense that the induced voltage at the anode of rectifier diode D1 is negative such that the rectifier is reverse biased and no current flows in the secondary winding while the magnetic field is increasing. The number of turns for the secondary winding may be larger than that of the
25 primary in order to provide a voltage boost function.

- When the control circuit 34 turns off transistor Q1 the magnetic field begins to collapse, causing voltages of opposite polarity to be induced across both the primary and secondary windings. Because these voltages are opposite in polarity, the anode of diode D1 is driven to a positive polarity, causing this rectifier to conduct and current to flow, thereby
30 delivering energy to secondary battery "B."

- The circuits labeled "Feedback" sample the voltage of the secondary battery "B". This feedback voltage is applied to a differential amplifier 35 whose output is provided to a

conventional window voltage comparator 36. The differential amplifier 35 senses the terminal voltage of battery "B" while maintaining electrical isolation between the negative circuits of batteries "A" and "B". The voltage output of differential amplifier 35 is representative of the terminal voltage of battery "B" regardless of any voltage difference between the battery "A" and battery "B" negative circuits.

- 5 • The output of differential amplifier 35 is also applied to the Feedback input of the control circuit 34 to vary the rate of switching of transistor Q1. This provides the means to regulate the flow of energy to the secondary battery "B" and, hence, the charge imparted to that battery.
- 10 • Window voltage comparator 36 compares the battery "B" voltage against two predetermined reference voltages "Vth_low" and "Vth_high" which, by way of example, might be 3.7 and 3.9 volts respectively. When the terminal voltage of battery "B" falls below "Vth_low" the first comparator output is asserted. This output is connected to the "Set" input of flip-flop 38 so that the flip-flop is toggled to the "set" state.
- 15 • Similarly, when the terminal voltage of battery "B" rises above "Vth_high" the second comparator output is asserted. This output is connected to the "Reset" input of flip-flop 38 so that the flip-flop is toggled to the "reset" state. This configuration results in flip-flop 38 being toggled to the "set" state when the battery "B" voltage falls below a first predetermined value and being toggled to the "reset" state when the battery "B" voltage rises above a second predetermined value. This function provides the control required to enable the pulse control circuit 34 only if the battery "B" voltage falls below a minimum value and to continue to charge battery "B" only until its terminal voltage has risen to a maximum acceptable value. At that point the "Enable" signal will be negated to prevent overcharging of battery "B".
- 20 • The flip-flop 38 is implemented with metal oxide semiconductor field effect transistors (MOSFETs) or similar devices which require little or no bias current to effect device switching in order to reduce quiescent current drain to an absolute minimum.
- 25 • An additional benefit of this circuit not previously disclosed is provided by the pulse generator 39. The charge control circuitry must derive prime power from one of the two batteries, in this case battery "A". In order to minimize the current drain on battery "A" needed to support the circuitry, the voltage reference 37 and window comparator 36 are powered by the output of the pulse generator 39. The pulse generator 39 is similar to

oscillator circuits well known in the pacemaker industry, whereby energy is slowly extracted from a battery and used to charge a capacitor. The capacitor is then rapidly discharged to deliver a higher amount of power in a shorter period of time. In this embodiment the pulse generator 39 output is used to power the voltage reference 37 and window comparator 36 circuitry so that the circuitry is only energized periodically, e.g. once per minute.

By intermittently energizing the voltage reference 37 and window comparator 36 and controlling the state of the control circuit 34 with a low power flip-flop 38, this embodiment significantly reduces the quiescent current drain on the battery and therefore provides a 10 significant improvement in service life.

The primary benefit of the hybrid battery power source 30 of Fig. 4 relative to the Fig. 2 design is that the primary and secondary batteries "A" and "B" do not share a common return circuit.

The voltage boost/charge regulator circuits described in the embodiments of Figs. 2 15 and 4 are capable of providing charging currents for the secondary battery "B" in the range of milliamperes to hundreds of milliamperes. This characteristic provides for rapid recharging of the secondary battery but also requires the primary battery "A" to supply significant discharge current. Primary battery chemistries such as Li/CF_x and Li/MnO₂ are capable of supporting these discharge rates but Li/I batteries cannot support discharge currents above 20 100 microamperes because of their inherently high internal resistance. In some cases, it may be desirable to use a Li/I chemistry for the primary battery "A" of an implantable defibrillator or ICD insofar as such batteries are already commonly used for implantable pacemakers and are commonly available.

Turning now to Fig. 5, another exemplary hybrid battery power source 40 is shown 25 that will allow the use of a Li/I primary battery. The power source 40 includes a primary battery "A" and a secondary battery "B" as in Fig 1. Battery "A" and battery "B" are connected in parallel with a voltage boost/charge control circuit 42. In the embodiment of Fig. 5, the charging current for the secondary battery "B" can be applied using pulses that are shorter in duration than is practical for the embodiments of Figs. 2 and 4. This reduces the 30 charging rate for the secondary battery "B" but also reduces the discharge rate of the primary battery "A" to a level that is compatible with Li/I primary cells. The embodiment of Fig. 5.

would thus be entirely suitable for an application where it is not necessary to recharge the secondary battery "B" in a period of less than two to five days.

As was the case with the embodiments of Figs. 2 and 4, the voltage boost/charge control circuit 42 is powered by primary battery "A" and provides a higher output voltage for the purpose of charging secondary battery "B." The circuitry also monitors the voltage of secondary battery "B" to control the charging current in order to maintain the secondary battery within the desired range of state of charge. The operation of this circuitry is as follows:

- A pulse generator circuit 43 is powered by primary battery "A" to produce a periodic output voltage pulse with a duration of one millisecond and a repetition period of one to two seconds. This circuitry is exemplified by pulse generators utilized in implantable pacemakers.
- The voltage output pulse is provided as prime power to the voltage references 44 and window comparator 45 such that they are operational only for the duration of the voltage pulse. This intermittent operation serves to significantly reduce the power consumption of the circuitry.
- The voltage references 44 provide two stable reference voltages, e.g. $V_{thr_low} = 3.6$ volts and $V_{thr_high} = 3.9$ volts.
- The voltage of secondary battery "B" is provided as an input to the window comparator 45 in order to affect the operation of the circuitry based upon the state of charge of the secondary battery. This feedback voltage is compared to the two stable reference voltages so that one comparator output changes state when the feedback voltage falls below the lower reference voltage and the second comparator output changes state when the feedback voltage rises above the higher reference voltage.
- The two outputs of the window comparator 45 are connected to the "Set" and "Reset" inputs of flip-flop 46 such that the flip-flop is toggled to the "SET" state when the secondary battery "B" voltage falls below the lower reference voltage and, conversely, the flip-flop is toggled to the "RESET" state when the secondary battery "B" voltage rises above the higher reference voltage.
- The output "Q" of the flip-flop 46 is connected to the "Enable" input of a voltage booster circuit 47 so that the voltage booster is operational only when the flip-flop 46 is in the "SET" state. In this configuration the voltage booster 47 will be operational only after

the secondary battery "B" voltage has fallen below the lower reference voltage and only until the secondary battery "B" voltage rises above the higher reference voltage.

- The voltage booster circuit 47 is triggered by the pulse generator 43 so that the voltage booster circuit produces output voltage pulses to transfer energy to the secondary battery "B."

5 Referring now to Fig. 6, the operation of the flip-flop 46 and voltage booster 47 is described in additional detail, as follows:

- When flip-flop 46 is in the "RESET" state, transistor Q2 is biased on, resulting in the flow of collector current and a low voltage on Q2 collector due to voltage drop across resistor R6. No current flows in resistor R7 to bias transistor Q1, so the collector voltage of Q1 is high, allowing current flow through resistor R5 so that transistor Q2 remains biased on. The "SET" input is negated so that its voltage is approximately equal to the "RTN" return voltage.
- If the "SET" input is momentarily asserted by raising the applied voltage, diode D1 will be forward biased and conduct, causing the base voltage of Q1 to rise and collector current to flow through resistor R2. When Q1 is no longer biased off the collector voltage will fall due to voltage drop through resistor R2 and no current will flow through resistor R5, eliminating the bias current for Q2. Q2 will cease to conduct collector current, allowing the collector voltage to rise and supply bias current for Q1 through resistor R7.
- Conversely, if Q2 is biased off and the "RESET" input is momentarily asserted by raising the applied voltage, the base-emitter junction of transistor Q2 will be forward biased and Q2 will conduct collector current. This current will cause a voltage drop in resistor R6 so that the collector voltage of Q2 falls, causing Q1 to be biased off because bias current no longer flows through resistor R7. With Q1 biased off the collector voltage rises and allows bias current for Q2 to flow through resistor R5. The flip-flop will therefore remain in either the "SET" or "RESET" state indefinitely until a trigger pulse is applied to the converse input.
- While flip-flop 46 is in the "SET" state current will flow through resistor R8 to the base of transistor Q4 so that Q4 is biased on. Current will flow through the collector of Q4 and through D3, C1 and R11 so that a charge accumulates on capacitor C1.
- Each time that a positive voltage pulse is applied to the "PULSE" input of the circuit, the

base-emitter junction of Q3 will be forward biased and Q3 will conduct current through its collector terminal. This current will be supplied by resistors R9 and R10.

- When current flows through R9 and R10 the voltage drop across R9 will cause the base-emitter junction of Q5 to be forward-biased so that Q5 will be biased on. This will lift the collector voltage to a value approximately 0.3 volts less than the emitter voltage.
- Capacitor C1 will be charged to the full supply voltage Vpri between the voltage pulses applied to the "PULSE" input. Each time that the "PULSE" input is asserted and transistor Q5 is biased on, the lower potential end of C1 will be pulled up by the collector of Q5. The higher potential end of C1 will rise to almost twice the supply voltage Vpri.
- When the higher potential end of C1 rises above Vpri diode D3 will be reversed biased and the base-emitter junction of transistor Q6 will be forward biased so that energy stored in C1 will flow through Q6 to battery "B."
- When the "PULSE" input is negated, transistors Q3 and Q5 will be biased off and C1 will be recharged through Q4, D3 and R11.

The circuit of Figs. 5 and 6 thus provides a pulsed output voltage that is nearly twice the amplitude of the supply voltage under the control of the voltage references 44 and window comparator 45.

Turning now to Fig. 7, a representative graph of cell voltage and charging current versus time is shown. As previously discussed, the long-term performance of the lithium-ion cell is strongly dependent upon the manner in which it is charged. Specifically, the energy storage capability of a cell is a maximum when the cell has been charged to its final voltage and maintained at that voltage for some period of time as the charging current is decreased. This concept is discussed by Buchmann, I. in an article entitled "Improve Battery Performance with Proper Charging Methods", *Medical Electronics Manufacturing*, 35 – 38, (Fall 2003). The time-varying charging current control feature can be incorporated into the voltage boost/charge control circuit of the present invention in order to further improve the performance of the secondary lithium-ion battery.

Fig. 8 illustrates an exemplary hybrid battery power source 50 that provides time-varying charging current control capability. This power source includes the primary battery "A" and the secondary battery "B" of Fig. 1. Battery "A" and battery "B" are connected in parallel with a voltage boost/charge control circuit 52 that includes the previously described circuit functions of a pulse generator 53, voltage references 54, window comparator 55 and a

flip-flop 56. The voltage boost oscillator circuitry 57 embodies those functions previously discussed in Figs. 2 and 4. An amplifier 58, sample-and-hold (S/H) circuit 59, comparator 60, binary counter 61 and digital-to-analog (D/A) converter 62 have been added to provide improved functionality as explained below.

5 The inputs of amplifier 58 are connected to the terminals of secondary battery "B" to monitor the battery voltage and hence, SOC. The output of amplifier 58 is applied to the S/H circuit 59 and voltage comparator 60. The S/H circuit 59 is periodically triggered by an output from the pulse generator 53 to sample the amplified battery voltage. The output of the S/H circuit 59 is representative of the last sampled battery voltage until a subsequent sample
10 is taken. The present battery voltage is compared against the stored value of the battery voltage in voltage comparator 60 so that the comparator output will toggle to indicate the trend of the terminal voltage of the secondary battery "B".

15 The output of voltage comparator 60 is applied to the UP/DOWN control input of binary counter 61 and the output of the pulse generator is connected to the clock input of the counter. In this manner the counter will either increment or decrement on each pulse output from the pulse generator 53. If the circuit is configured to increment the counter 61 when the secondary battery "B" voltage is decreasing over successive samples, the counter will increment. The digital outputs of the counter 61 are applied to a D/A converter 62 which converts the digital value of the counter to an analog voltage that rises and falls with the
20 digital count value. This voltage is applied to the control input of the oscillator 57 such that a higher control voltage will result in a higher recharging rate for the secondary battery "B". This added functionality will adjust the recharging rate of the secondary battery based upon the rate of discharge to provide improved performance.

25 Fig. 9 illustrates another exemplary hybrid battery power source 70 that provides time-varying charging current control capability. This power source includes the primary battery "A" and the secondary battery "B" of Fig. 1. Battery "A" and battery "B" are connected in parallel with a voltage boost/charge control circuit 72 that includes the previously described circuit functions of a pulse generator 73, voltage references 74, window comparator 75 and a flip-flop 76. The voltage boost oscillator circuitry 77 embodies those
30 functions previously discussed in Figs. 2 and 4.

In addition to the previously described circuit functions, an analog-to-digital (A/D) converter 78, a microprocessor system 79 and a digital-to-analog (D/A) converter 80 have

been incorporated into the hybrid battery power source design. The terminal voltage of the secondary battery "B" are provided as inputs to the A/D converter 78 so that the cell voltage may be provided as digital data to the microprocessor system 79. The A/D converter 78 will be periodically triggered by the microprocessor system 79 to provide data samples as

5 required. The microprocessor system 79 also has a digital output port that is connected to the inputs of the D/A converter 80. The digital data provided to the D/A converter 80 will represent the magnitude of charging current that will be supplied to the secondary battery "B". These circuit functions allow the microprocessor system 79 to sample the secondary cell voltage on a periodic basis to determine the SOC and time rate-of-change of the
10 secondary battery "B". The microprocessor system 79 may then alter the charging current (and hence, recharging rate) of the secondary battery "B" based upon sampled charge/discharge trends. Note that the microprocessor system 79 could be independent from a microprocessor system that controls the host medical device, or it could be part of the same system.

15 Turning now to Fig. 10, exemplary firmware logic is shown that may be programmed into the microprocessor system 79 of Fig. 9 for use in controlling charging current to the secondary battery "B." Beginning in steps 81 and 82, the microprocessor system 79 will obtain digital data samples of the secondary battery "B" voltage at a suitable rate, such as once per minute, and compute the present SOC. In steps 83, the microprocessor system 79
20 uses the collected data samples collected over a longer period of time, e.g. 15 to 20 minutes, to compute the trend in the SOC. In step 84, the SOC trend is compared to a threshold rate and a charge rate is set in steps 85 and 86 depending on the trend. For an application in an ICD, this trend will be strongly dependent upon the level and frequency of therapy that has been required. As an example, an ICD in a patient requiring few or no defibrillation shocks
25 over the period of a year would extract little or no energy from the secondary battery "B" and change in SOC would be minuscule. If the secondary battery cell voltage reached the lower SOC threshold such that the secondary battery "B" requires recharging, a low recharging rate would be sufficient to replenish the cell. This lower charge rate is set in step 85. Conversely, an ICD in patient experiencing a ventricular fibrillation "storm" may require multiple
30 defibrillation shocks in a period of a few hours. In this case the SOC of the secondary battery "B" would decrease rapidly and the microprocessor system 79 would select a high recharging current to replenish the secondary battery rapidly in order to better support the high rate

discharge activity. This higher charge rate is set in step 86. Step 87 represents a charging period following which the microprocessor logic returns to step 81.

Fig. 11 illustrates another exemplary hybrid battery power source 90 that provides time-varying charging current control capability. This power source includes the primary 5 battery "A" and the secondary battery "B" of Fig. 1. Battery "A" and battery "B" are connected in parallel with a voltage boost/charge control circuit 92 that includes the previously described circuit functions of a pulse generator 93, window comparator 94 and a flip-flop 95. The voltage boost oscillator circuitry 96 embodies those functions previously discussed in Figs. 2 and 4.

10 In addition to the previously described circuit functions, an A/D converter 97, microprocessor system 98, and D/A converters 99, 100 and 101 are incorporated into the circuitry. The terminal voltage of secondary battery "B" is applied to the inputs of A/D converter 97 in order to provide a digital representation of the battery voltage to the microprocessor system 98. As previously discussed, the microprocessor system 98 will 15 implement firmware logic such as that provided in Fig. 10. The microprocessor system 98 will produce digital output data which is applied to D/A converter 99 in order to produce an analog control voltage that affects control of oscillator 96 to vary the recharging rate of secondary battery "B".

This embodiment provides further control of the SOC of the secondary battery "B" by 20 replacing the fixed upper (V_{th_high}) and lower (V_{th_low}) voltage thresholds used in previous embodiments with D/A converters 100 and 101. Logic implemented in the firmware of microprocessor system 98 will utilize the sampled voltage measurements from the secondary battery and develop digital representations for the upper and lower window comparator thresholds based upon the SOC and time rate-of-change of the SOC of the 25 secondary battery. For example, these adjustable thresholds may enable the circuitry to minimize the self-discharge of the secondary battery over the lifetime of the hybrid battery system. By replacing the fixed thresholds with adjustable thresholds, this embodiment provides still more refined control of the SOC of secondary battery "B" and thus provides improved overall performance of the hybrid battery power source 90.

30 Turning now to Fig. 12, a battery powered high energy medical device is embodied as a defibrillation system 110 that is shown schematically to include a hybrid battery power source 112 and other circuits and devices for defibrillating a heart 114 using high voltage

pulses. The hybrid battery power source 112 includes the primary and secondary batteries "A" and "B" described above. Batteries "A" and "B" are preferably connected in parallel via one of the voltage boost/charge control circuits 22 or 32 or 42 or 52 or 72 or 92 as respectively described above relative to Figs. 2, 4, 5, 8, 9 and 11. The defibrillation system

5 112 includes physiologic electrodes 116 that are attached to cardiac tissue of the heart 114 for the purposes of monitoring cardiac activity and delivering electrical energy in the event that cardiac activity becomes abnormal. The electrodes are connected to the defibrillation system 112 via conventional leads 118. A conventional voltage amplification system or flyback converter circuit 120 having an oscillator-type control circuit 122 therein is also provided as 10 part of the defibrillator system 112. A description of defibrillation operation for the depicted subsystems follows:

- Circuitry (not shown) that is part of the defibrillation system 112 will monitor the activity of the cardiac muscle. Should the monitored cardiac activity indicate the need for delivery of a defibrillation charge, the signal labeled "Charge Enable" will be asserted to 15 activate the control circuit 122 that is part of the flyback converter circuit 120. This will begin a capacitor charge sequence for charging energy storage capacitors C2 and C3.
- The control circuit 122 derives prime power from the batteries "A" and "B" and includes an oscillator function. Capacitor C1 provides high frequency decoupling of, and lowers the AC source impedance for, the hybrid battery power source 112 to the flyback 20 converter 120. As stated, the hybrid battery power source 112 can be constructed in accordance with any of the previously described embodiments or modifications thereof. The control circuit output labeled "Switch" is toggled rapidly (e.g. 100 kHz) to turn transistor Q1 on and off.
- Each time that the transistor is turned on, the current in the primary winding of 25 transformer T1 increases and energy is stored in the magnetic field associated with the transformer. The connection of the secondary windings is reversed in the sense that the induced voltages at the anodes of rectifier diodes D1 and D2 is negative such that the diodes are reverse biased and no current flows in the secondary windings while the magnetic field is increasing.
- When the control circuit 62 turns off transistor Q1 the magnetic field begins to collapse, 30 causing voltages of opposite polarity to be induced across both the primary and secondary windings. Because these voltages are opposite in polarity, the anodes of diodes D1 and

D2 are driven to a positive polarity, causing the diodes to conduct and current to flow, delivering energy to energy storage capacitors C2 and C3.

- The number of turns in the secondary windings is much larger than that of the primary, by a ratio of perhaps 100 to 1, in order to achieve a large voltage boost function. Because of the large turns ratio, the low primary winding voltage will be increased to a high secondary winding voltage, the multiplication factor being nearly equal to the turns ratio. In this manner, a four volt prime power source may be increased to hundreds of volts.
- Two galvanically isolated secondary windings on transformer T1 have an identical number of turns and are each connected to energy storage capacitors C2 and C3. This connection causes the capacitors to be simultaneously charged in a parallel manner.
- At the completion of the charge cycle, the defibrillation system control circuitry negates the signal labeled "Charge Enable" and asserts the signal labeled "Defibrillate Command." This signal activates the high voltage switch 124 depicted as two switch circuits S1a and S1b. These switch circuits connect the energy storage capacitors C2 and C3 in a series configuration to the wires leading to the physiologic electrodes attached to the cardiac tissue. The energy stored in the capacitors C2 and C3 is rapidly delivered to the heart to provide the therapeutic benefit.
- Subsequently, the S/H circuitry 58 – 62 shown in Fig. 8 would establish the required recharging rate for the most efficient replenishment of the secondary battery.
- Alternatively, the microprocessor system 79 of Fig. 9 or 98 of Fig. 11 will monitor the SOC of secondary battery "B" and establish the required recharging rate in order to replenish the secondary battery at the most efficient rate.

The defibrillator system of Fig. 12 thus utilizes a flyback converter 120 that has primary and secondary transformer windings in the circuit providing isolation from the hybrid battery power source 112. This allows a modification in which multiple transformers can be used, with the primary sides thereof being connected in series or parallel and the secondary sides thereof likewise being connected in series or parallel. This would provide voltage multiplication in case it is desired to multiple capacitor banks.

Rationale for Configuration

The configuration of batteries and circuitry described above in connection with the various drawing figures, provides an improvement in the performance of implantable

defibrillators and ICDs by reducing charge times to manageable and predictable levels. These configurations provide the additional benefit of utilizing the stored energy of the primary battery to maximum extent, thereby increasing the service life for the defibrillator system. As previously described herein, a fundamental requirement for the power source in an implantable defibrillator/ICD application is the ability to deliver a large amount of energy to the circuitry rapidly in order to charge the energy storage capacitors in the shortest time possible. A second requirement of nearly equal importance is the maximum utilization of stored energy within the power source in order to provide maximum service life for the implanted device.

The rate at which power can be delivered from a battery or other electrical energy source to a load is inversely proportional to the internal resistance or impedance of the energy source. This is due to the fact that the load current flows through the internal resistance of the battery and the resulting power is dissipated as waste heat within the battery structure. In order to reduce the time required to charge the energy storage capacitors, the charging circuit must draw higher load current from the battery. All of the primary battery chemistries discussed above produce an internal resistance that renders them less able to supply high peak currents as well as the lithium-ion battery. This can be seen in Table 1 below.

Table 1

Battery Chemistry	Voltage Range (VDC)	Internal Resistance (ohms)	Energy Density (Wh/cm ³)
Lithium – CF _x (primary)	2.8 – 2.0	5 – 10	0.9 – 1.0
Lithium – BrCl ₂ (primary)	3.5 – 3.0	20 – 30	0.8
Lithium – MnO ₂ (primary)	3.0 – 2.5	1 – 5	0.6
Lithium – SO ₂ Cl ₂ (primary)	3.5 – 3.0	10 – 20	0.9
Lithium – SOCl ₂ (primary)	3.5 – 3.3	10 – 20	0.9
Lithium – SVO (primary)	3.0 – 2.0	0.5 – 1.0	0.7
Lithium – Ion (rechargeable)	4.0 – 3.5	0.1 – 0.5	0.3

The internal resistance values given above are average values. It will be seen that the internal resistances of the Li/CFx, Li/BrCl₂, Li/MnO₂, Li/SO₂Cl₂ and Li/SOCl₂ battery chemistries are the highest among the primary batteries, and, with the exception of Li/MnO₂, this internal resistance renders such batteries unsuitable for defibrillators and ICDs if used alone. The internal resistance of the Li/SVO battery chemistry is substantially lower than that of the other primary batteries (with the exception of Li/MnO₂, which is relatively close). As noted above, however, the internal resistance of Li/SVO batteries can increase to unacceptable levels during battery run-down. These batteries are thus susceptible to voltage delay effects, it may be concluded that they are somewhat unreliable for implantable defibrillator and ICDs if used alone.

By comparison, the internal resistance of the lithium-ion rechargeable battery is low and relatively stable. However, it will be seen that its energy density is also substantially lower than that of the primary batteries. In regard to battery energy density and device service life, it is known that for a given battery volume, the highest energy density battery will possess the largest total energy and will, logically, provide the longest device service life. If the primary batteries are compared based upon energy density, the data in Table 1 indicate that the Li/CFx battery chemistry should provide the longest device service life, with the Li/BrCl₂, Li/SO₂Cl₂ and Li/SOCl₂ battery chemistries being the second best, followed by the Li/MnO₂ and Li/SVO batteries in third and fourth place, respectively. However, as indicated, the internal resistance characteristics of these batteries render them problematic if used alone in a defibrillator or ICD. The lowest energy density of all the above-listed battery chemistries is found in the lithium-ion rechargeable battery. The energy density level is so low that such batteries cannot realistically be used in implantable defibrillators and ICDs without recharging, notwithstanding their favorable internal resistance values.

The present invention overcomes the deficiencies of individual battery chemistries by combining two different battery chemistries wherein each battery is utilized in a manner that is optimally matched to its capabilities. An Li/CFx battery, Li/BrCl₂, Li/MnO₂, Li/SO₂Cl₂, Li/SOCl₂ or a Li/SVO battery can thus be chosen for the primary energy source (battery "A") because they provide superior total energy for a given battery volume. A lithium-ion secondary battery can be chosen as the energy source for the energy storage capacitors (battery "B") because it provides an internal resistance that is low and relatively stable, and is therefore capable of delivering the highest instantaneous power, providing the shortest

charging time for the energy storage capacitors. The embodiments that are taught herein thus provide a clear advantage over single battery systems because each battery is used in an optimum fashion, yielding an implantable defibrillator or ICD with minimum capacitor charging time and maximum device service life.

5 The present invention is also superior with respect to precise end-of-life determination of the power source. In particular, a monitoring feature could be provided that monitors the voltage level of the primary battery "A." Over time, this voltage level may drop below the level necessary to trigger the voltage boost circuit. Monitoring the voltage level of the primary battery "A" would provide a warning that the secondary battery "B" is no longer 10 being charged. However, if the secondary battery "B" is a lithium-ion cell, it will have a known capacity to deliver some number of defibrillatory pulses, say 50 pulses. It would thus be known that the defibrillator or ICD will work for this remaining number of pulses and steps can be taken to promptly replace the primary battery "A."

15 This present invention is also superior to the concepts disclosed in prior art that embodies multi-battery systems with respect to the management of energy transfer from a primary energy stage to a secondary energy stage. Whereas the cited prior art teaches the use of simple continuous trickle charging circuits or the use of voltage doubler circuits in conjunction with a second stage rechargeable battery, we teach the use of switch mode 20 voltage boost/charge control circuit with feedback that provides recharging under controlled conditions to optimize the performance of the lithium-ion chemistry system utilized for the secondary battery "B."

Accordingly, a hybrid battery power source for implantable medical use has been disclosed and the objects of the invention have been achieved. It should, of course, be understood that the description and the drawings herein are merely illustrative, and it will be 25 apparent that the various modifications, combinations and changes can be made of these structures disclosed in accordance with the invention. For example, it will be appreciated that conventional telemetry control circuitry could be used to control operation of the circuits shown in the figures. Settings that could be adjusted in this fashion would include the voltage references V_{thr_low} and V_{thr_high} , as well as pulse generator duty cycle. It should 30 be understood, therefore, that the invention is not to be in any way limited except in accordance with the spirit of the appended claims and their equivalents.